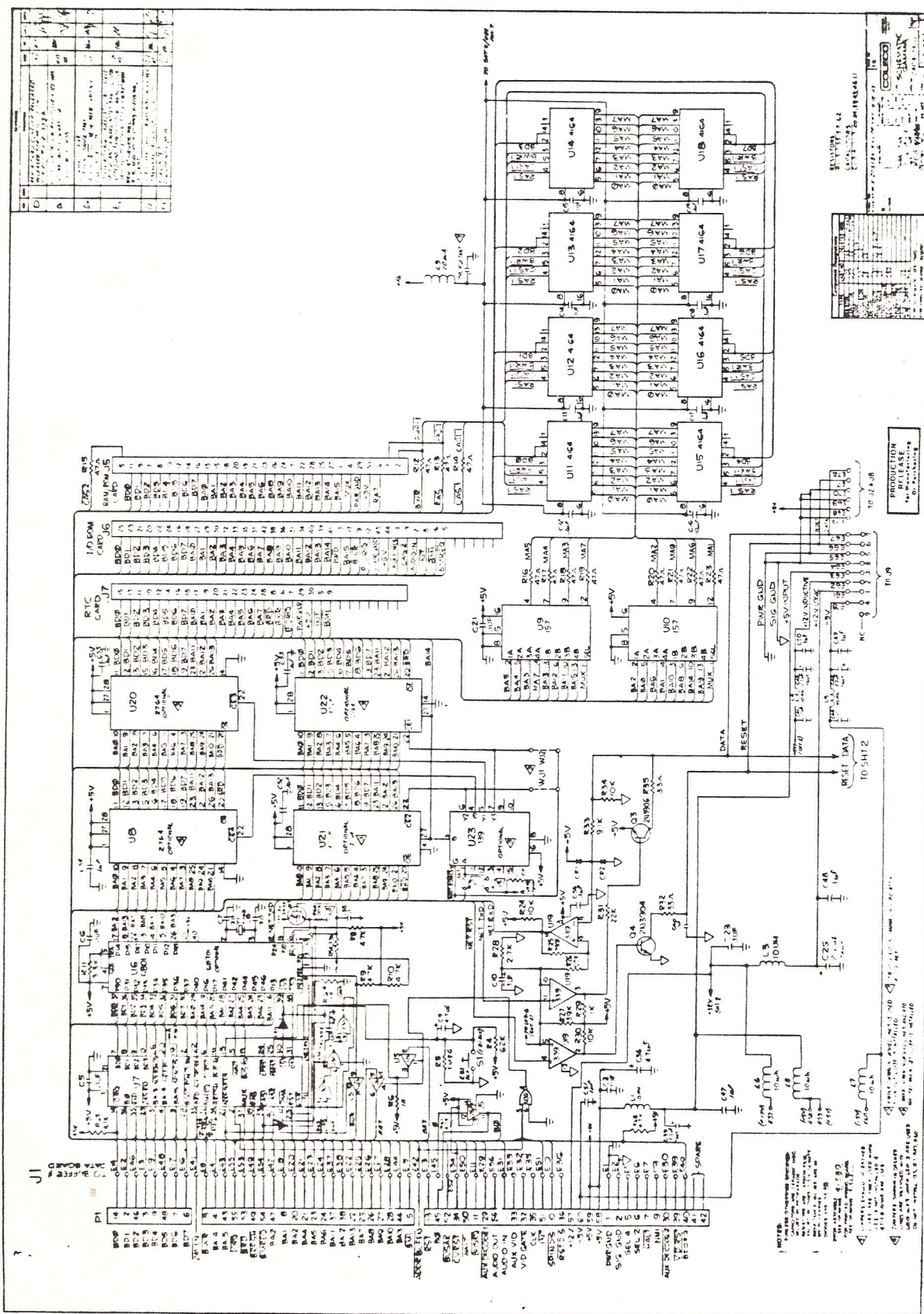
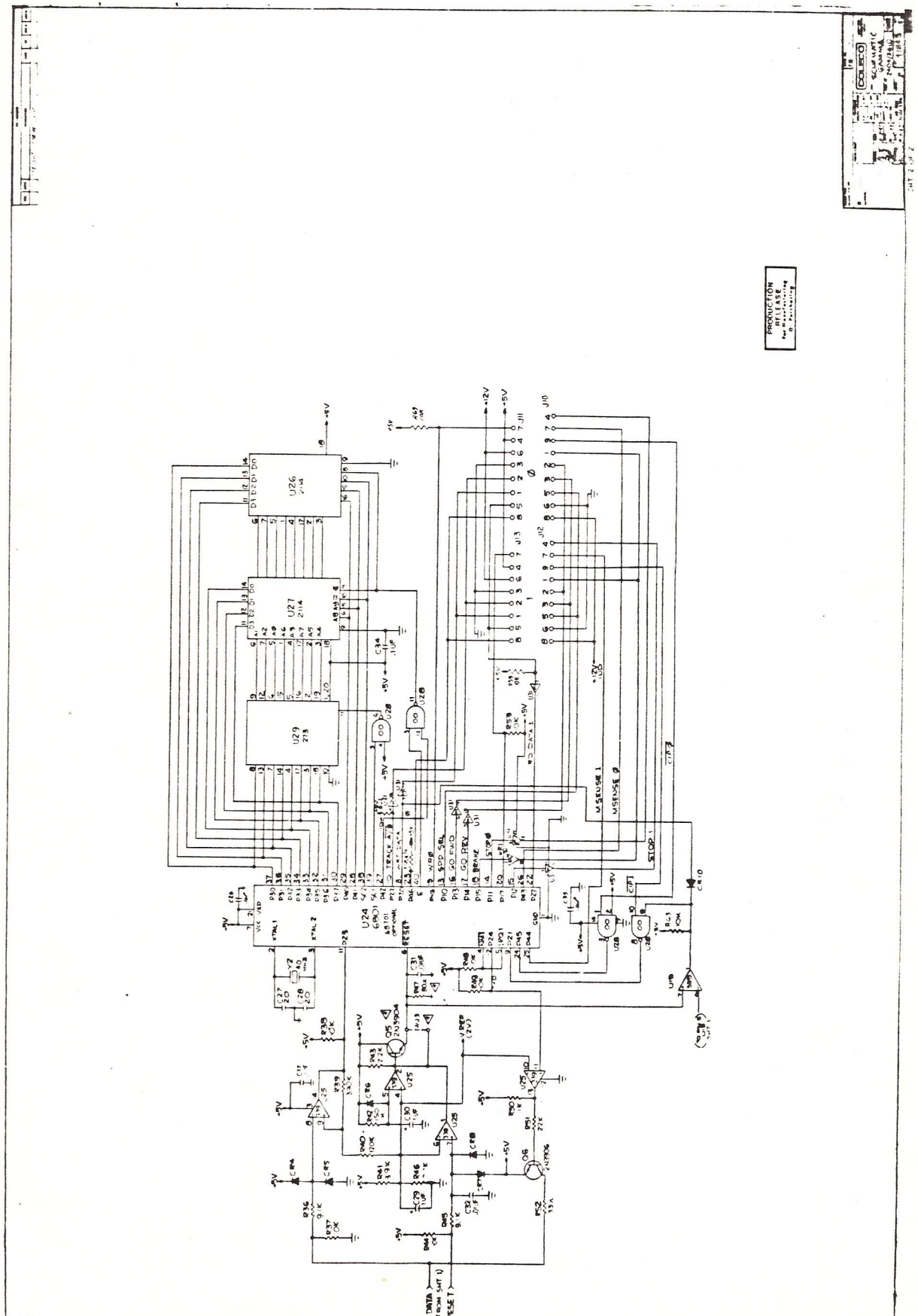


4.       Schematics and Component Location/Identification Drawings

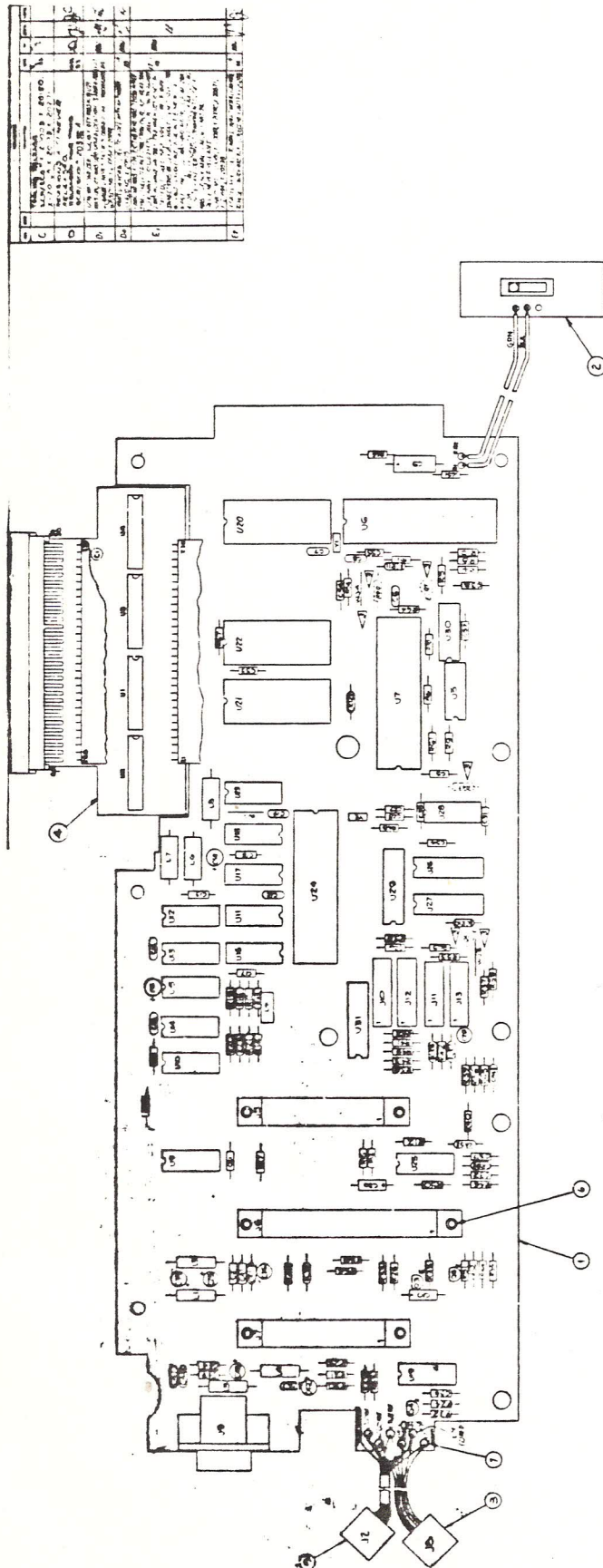
#### 4.1 Memory and I/O Board Schematic (Sheet 1)



# 4.1 Memory and I/O Board Schematic (Sheet 2)



4.1

[illegible]

**NOTE ASSEMBLY PER CONSO**

**TELESCOPIC**

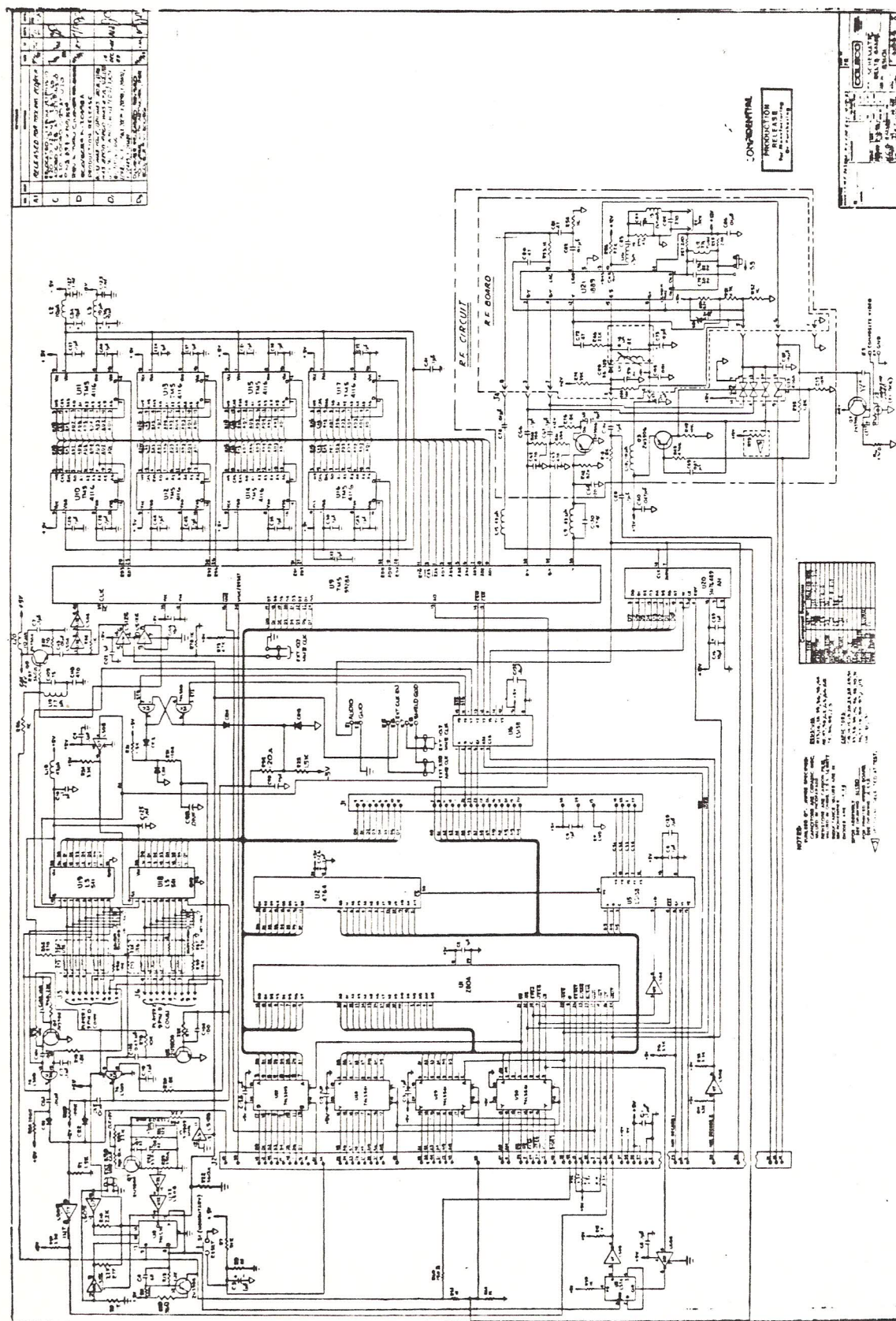
**PRODUCTION  
RELEASE**  
For Manufacturing  
Or Purchasing

TV LINECUTTING

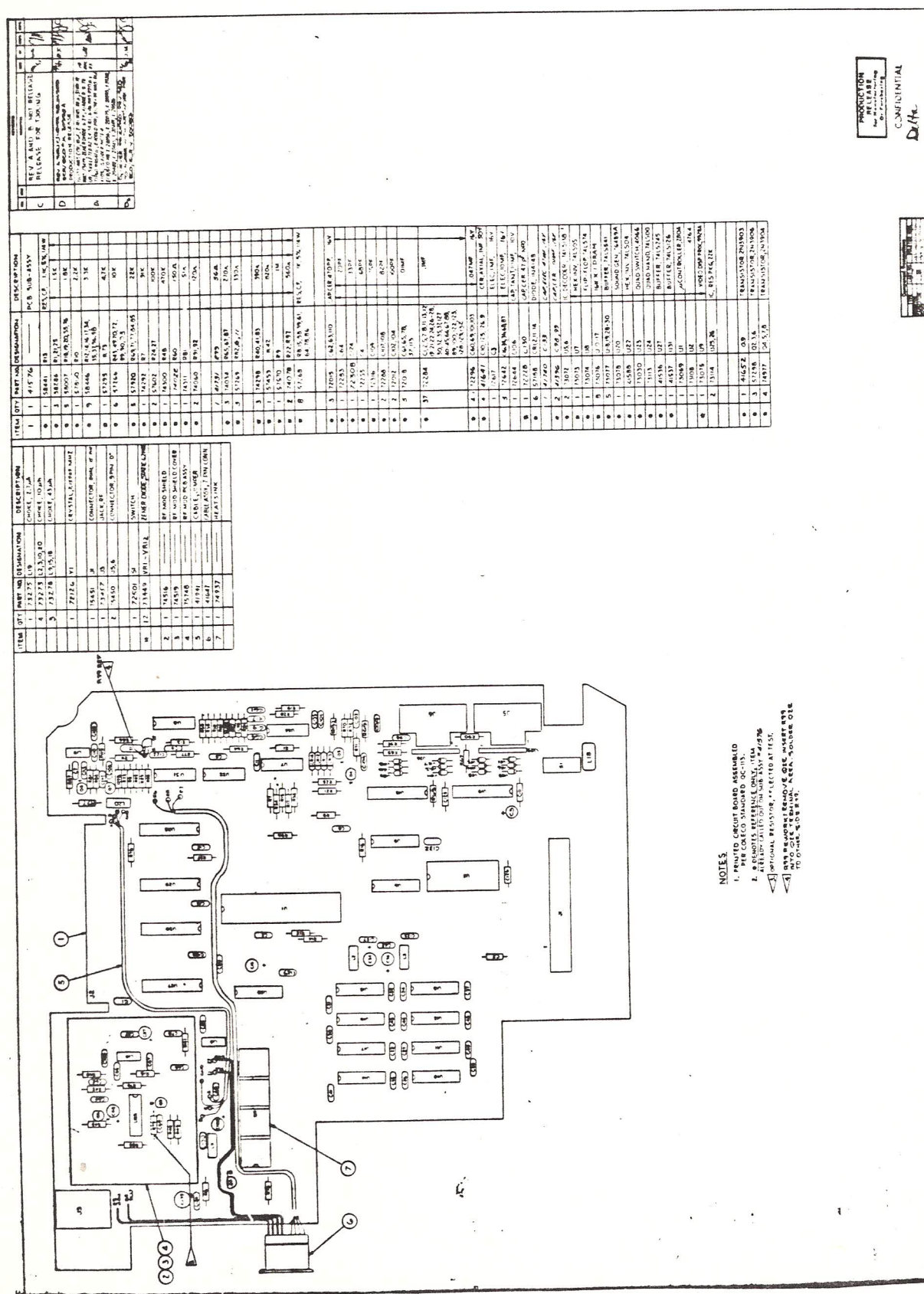
CAMPA

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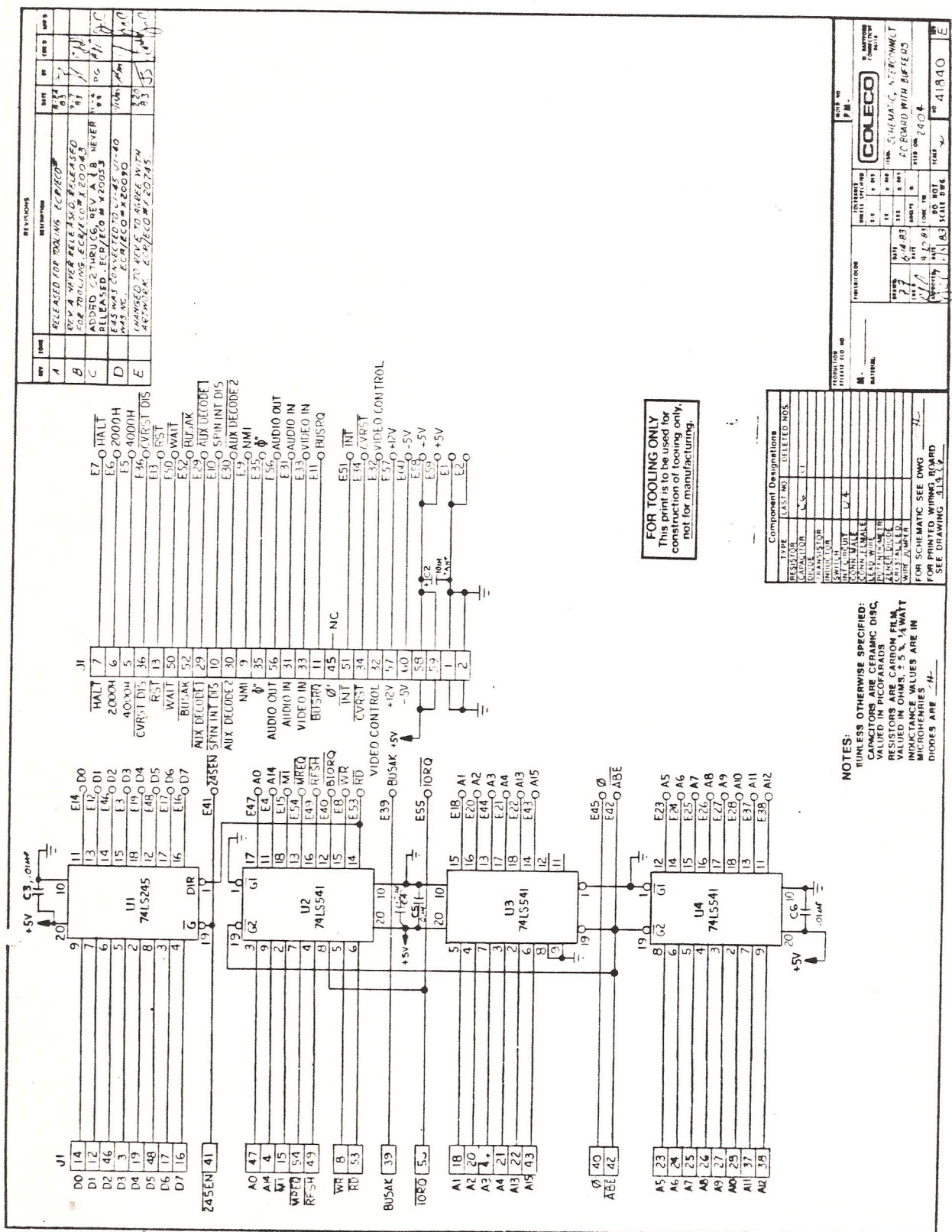
## 4.2 CPU Board Schematic



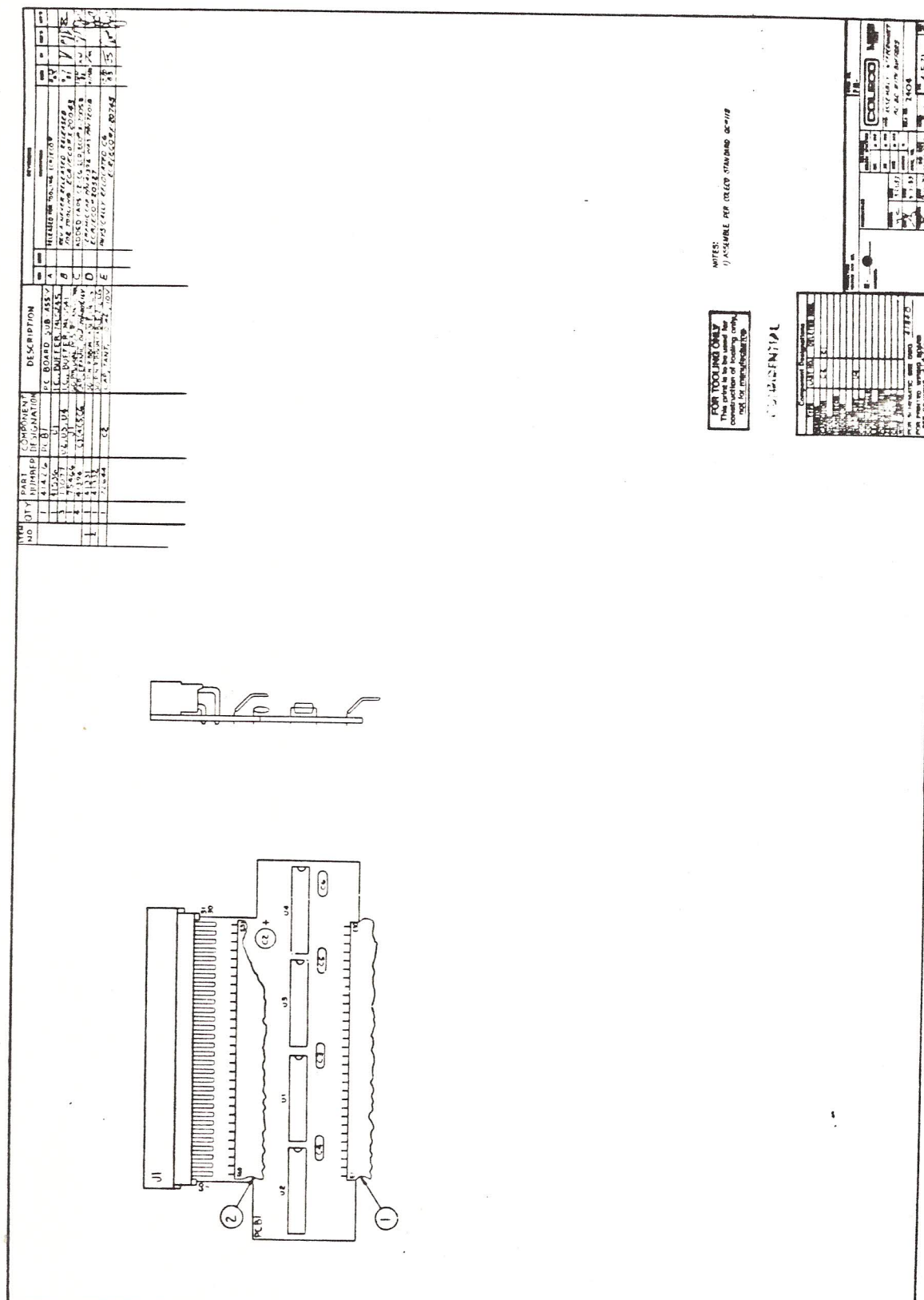
#### 4.2 CPU Board Component Location/Identification Drawing



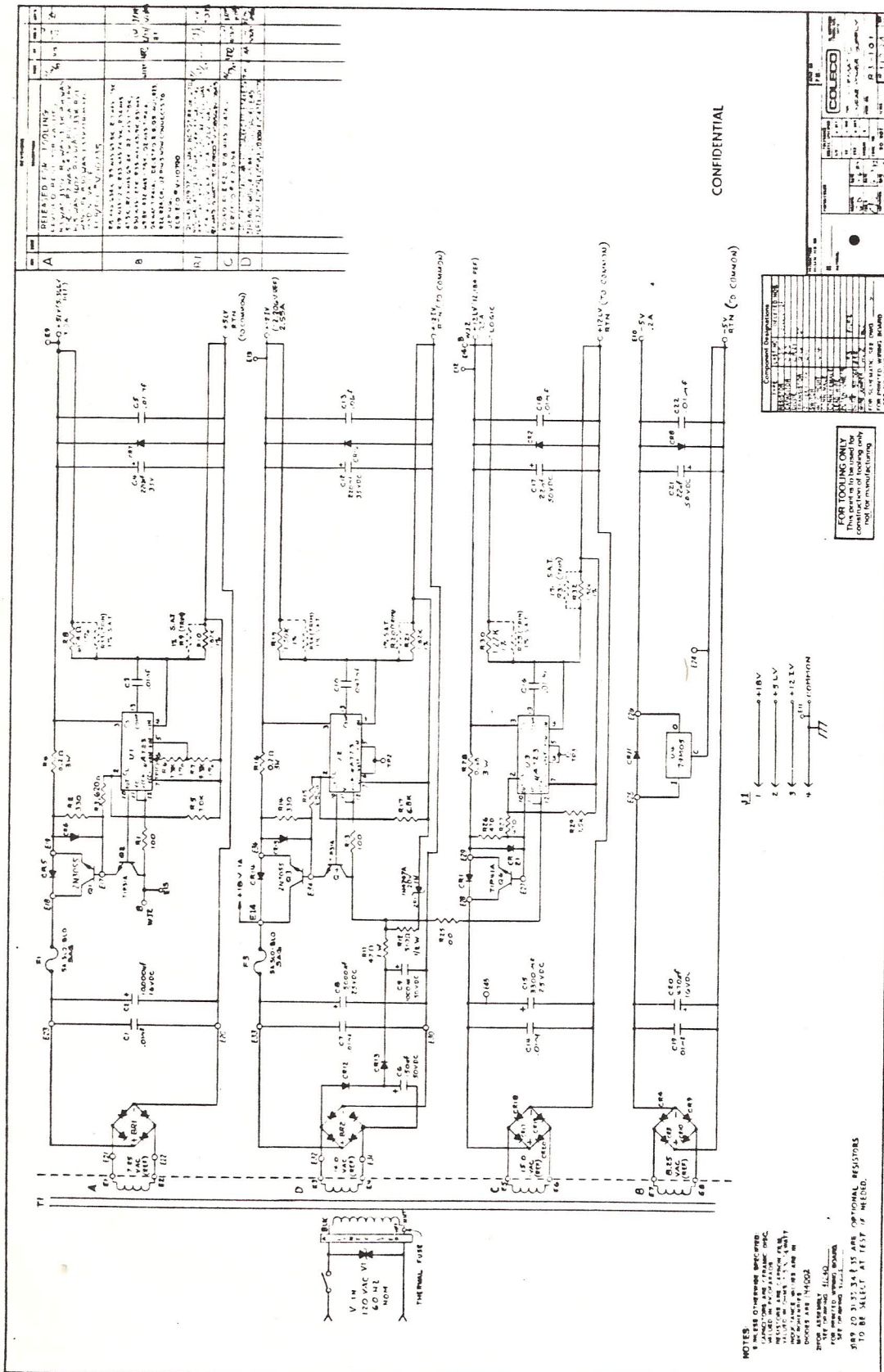
# 4.3 Interconnect Board Schematic



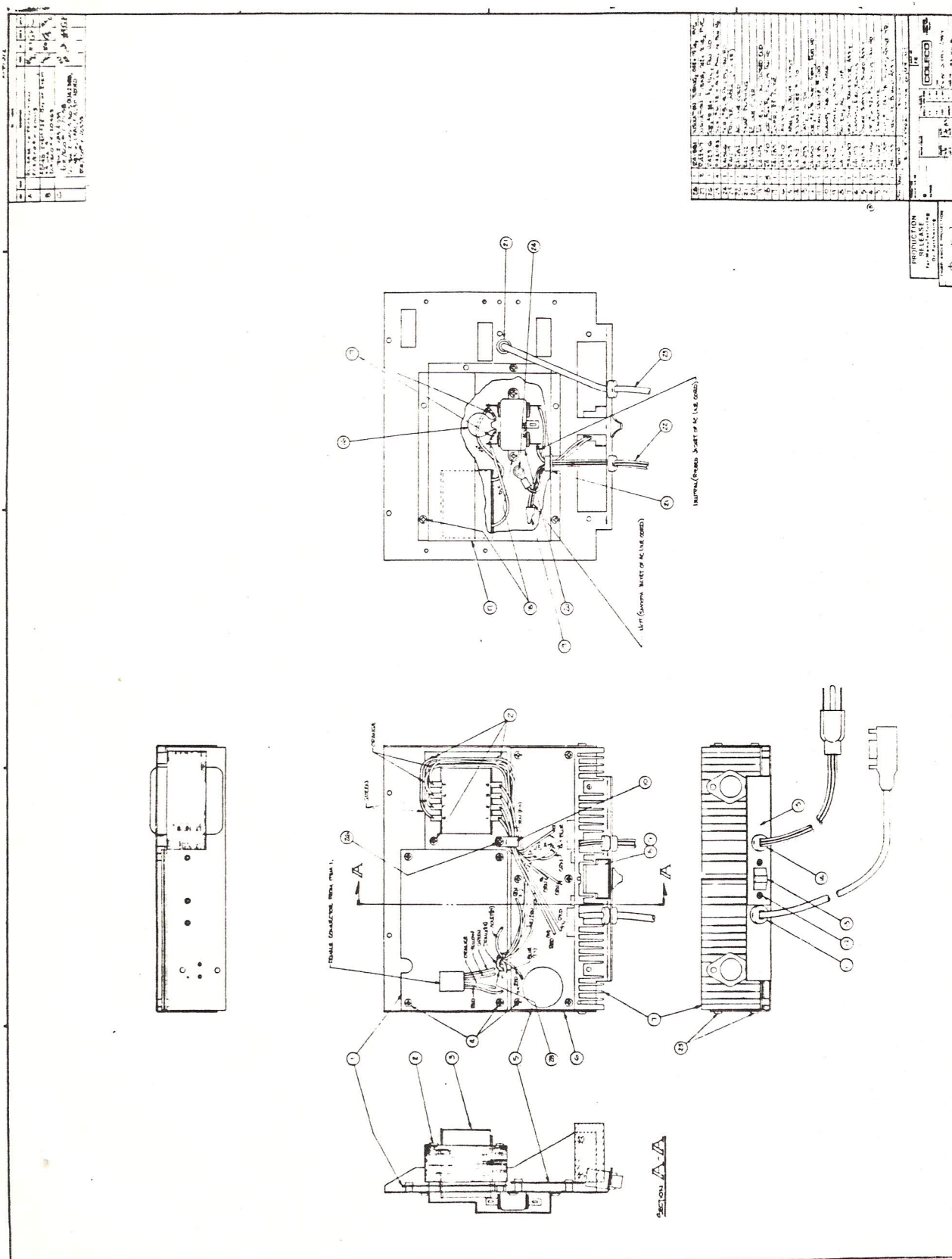
## 4.3



# 4.4 Linear Power Supply Schematic



4.4 Linear Power Supply Component Location/Identification Drawing



QTY	DESCRIPTION	QTY	DESCRIPTION
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3	4000	3	4000
4	4000	4	4000
5	4000	5	4000
6	4000	6	4000
7	4000	7	4000
8	4000	8	4000
9	4000	9	4000
10	4000	10	4000
11	4000	11	4000
12	4000	12	4000
13	4000	13	4000
14	4000	14	4000
15	4000	15	4000
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100	4000	100	4000